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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Hastings

Docket No:

TI-35919

Serial No:

10/615,354

Examiner:

TBD

Filed:

07/07/2003

Art Unit:

2816

For:

METHOD AND CIRCUIT FOR PERTURBING REMOVABLE SINGULARITIES IN

COUPLED TRANSLINEAR LOOPS

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97 & 1.98

Assistant Commissioner for Patents Washington, DC 20231

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450 on

Sharlet Hurst

Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. A copy of the noted references is enclosed herewith.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668, and please credit any excess fees to such Deposit Account. Any further necessary extension of time is hereby requested.

Respectfully submitted,

W. Daniel (Swayze, Jr.

Attorney for Applicants

Reg. No. 34,478

Texas Instruments Incorporated P.O. Box 655474, MS 3999 Dallas, TX 75265 (972) 917-5633

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				Application Number	10/615,354		
SEP 2 7 2004	SEP 2 7 200 NEC RMATION DISCLOSURE				7/7/2003		
STATEMENT BY APPLICANT				First Named Inventor	Hastings		
				Group Art Unit	2816		
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Sheet PADE	1	of	2	Attorney Docket No.	TI-35919		

				U.	S. PATENT DOCL	JMENTS
		U.S. Patent Document		Name of Patentee	Date of Pub.	
Exam. Initials*	Cite No.1	Number	Kind Code ² (if known)	or Applicant of Cited Doc.	of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	AA	6,074,082	-	Gilbert	06-13-2000	
	AB	4,323,797		Embree et al.	04-06-1982	
	AC	5,134,355		Hasings	07-28-1992	
	AD	2002/00185 26A1		Osaka et al.	02-14-2002	
	AE	1				
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	FOREIGN PATENT DOCUMENTS								
		Foreign Patent Document			Name of Patentee	Date of Pub.			
Exam. Initials*	Cite No.1	Office ³	Number⁴	Kind Code ² (if known)	or Applicant of Cited Doc.	of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ₆	
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Substitute for Form 1449A/PTO				Complete If Known		
				Application Number	10/615,354	
	INFORMATIO	N DISCLOSURE		Filing Date	7/7/2003	
STATEMENT BY APPLICANT				First Named Inventor	Hastings	
				Group Art Unit	2816	
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Sheet	2	of	2	Attorney Docket No.	Ti-35919	

		OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS	
Exam. Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	CA	HASTINGS A. "Integrated MOSFET interface for a synchronously-rectified SMPS" BIPOLAR/BICMOS CIRCUITS AND TECHNOLOGY METTING, 1995. PROCEEDINGS OF THE 1995 MINNEAPOLIS, MN, USA 2-3 OCT. 1995, New York, NY, USA, IEEE, US, October 2, 1995, pages 58-61, XP010159317 ISBN: 0-7803-2778-0 *page 60, right-hand column, line 16-page 61, left-hand column, line 2; figure 5*	
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